Alarm & Regulation

Input/Output Modules



DIGIcontrol-C8D, -C88

- Universal I/O extension module
- 4 universal inputs for counters, PT1000 or balanced inputs
- 4 universal inputs for PT1000, NTC 10k, 0-25mA, 0-12,5V or balanced inputs
- 8 potential free power relays with feedback & manual override
- Jog dial with graphic display (-C8D only)
- Configurable display for inputs, relays & info (-C8D only)
- power over bus or internal 90-230VAC power supply
- 180mA 24VDC output with SW resettable fuse
- V4 analogue reporting system
- Lighting control and alarm function
- High security AES encrypted Bus (IBB/S) interface



DIGIcontrol-C8D, -C88

The DIGIcontrol-C8D & -C88 is a universal input/output control module to extend the input and output capacity of DIGIcontrol-FC3xxx controller with light switches, presence detectors, regulation devices, alarm points and general purpose power relays over a secure AES encrypted IBB/S bus.

4 universal resistive inputs can be SW configured as balanced or unbalanced digital input, as counter inputs for energy measurement or water/gas metering, as PT500, PT1000 or NTC 10k.

4 universal selective inputs can be configured as balanced or unbalanced inputs, to measure Ω , 0-20mA, 0-10V or as temperature sensor with P500, PT1000 or NTC 10k.

New V4 analogue reporting system with individual intervals for actual-, mean, min. & max value of each input. A special wattmeter reporting system features current energy consumption and energy counter reporting. Measured values can be corrected with offset, multiplication and divide factor.

The 8 power relays with status feedback can be controlled by application or by manual overwrite by local jog-dial (-C8D) or host SW command. Relay status and manual overwrite is shown by dual colour LED and on LCD-Display (-C8D only). Overwrite is stronger than application commands.

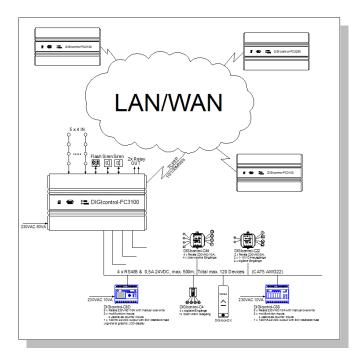
All inputs can be used for light control, alarm, access control and regulation functions for one or more different functions at the same time.

The device is powered with an integrated 15W 90-250VAC power supply or external 24VDC to prevent high bus power load from relays. An 180ma 24VDC output with SW resettable fuse allows direct supply of various sensors. The CPU is always powered over the bus to allow reporting of power fails.

The 1.8" LCD and jog dial of the DIGIcontrol-C8D allows easier diagnostic and maintenance. It show status and values of inputs and relay outputs. Relays can be set by jog-dial to manual overwrite with on, off or auto mode. Host manual overwrite has same priority as jog-dial. An additional info page allows showing network system values like outside temperature or system wide power consumption.

A unique serial number allows the configuration of a RS485 ID-number in the range from 1 to 120.

Input / output modules



Technical Data:

DIGIcontrol-C8D

32-bit ARM Cortex-M4 & ARM Cortex-M0+ CPU (Display) Memory: 256kb Flash & 32kB SRAM

Real time clock with super cap

IBB/S AES encrypted network interface

16Bit 250ksps ADC for

- 4 multifunction resistive or counter inputs
- 4 multifunction selectable resistive inputs
- 3 predefined sensors PT500, PT1000, NTC 10k

Ω, 0-20mA & 0-10V

Configurable offset with mul & div correction factor 8 Relay 230VAC/10A with manual overwrite & feedback inputs

8 Relay 230 VAC/10A with manual overwrite & feedb 12 Dual-LED's for relay & module status

1.8" colour graphic 128x160 pixel LCD-display with backlight Jog-Dial for LCD & relay control

Supply voltage: 90-250VAC 15VA

and 10-28 VDC from IBB Supply current: 40mA max. 180mA 24VDC Output for sensors with SW resettable fuse New V4 analogue reporting system

3 internal service inputs for supply, output and bus voltage Operating temperature: -10° to +45° C

Dimensions: W108 x H85 x D61 mm, DIN-Rail mounting

DIGIcontrol-C88

Same as DIGIcontrol-C8D but instead Jog-Dial & LCD-Display window with 8 Dual-LED's and space for output description label.

Delivery Contents:

DIGIcontrol-C8D or -C88 complete with installation and wiring instructions.

Information contained in this document is correct at the time of publication (070617) is subject to change without notice.



